

## **Core Modules in Communications Electronics**

Follow up Modules:

### **EI70510 Analog and Mixed-Signal Electronics, Brederlow, WS**

- EI71079 CMOS Analog to Digital Converters, SS
- EI7523 Analog Bipolar Electronics: Devices, Simulation and Circuits, WS
- EI78060 Lab CMOS A/D Converter Design, WS
- EI78064 Lab CMOS Voltage Regulation Circuit Design, WS/SS
- EI75503 Scientific Seminar on structure, architecture, and application of sensor circuits, WS/SS

### **EI70530 Embedded Systems and Security, Sigl, WS**

- EI71029 Physical Unclonable Functions, WS
- EI71073 Quantum Computers + Quantum Secure Communications, SS
- EI71070 Advanced Cryptographic Implementations, SS
- EI70520 Circuit Design for Security, SS

### **EI70610 Electronic Design Automation, Li/Tseng, WS**

- EI74042 Mathematical Methods of Circuit Design, WS/SS
- EI71059 Mixed Integer Programming and Graph Algorithms for Engineering Problems, WS
- EI70640 Synthesis of Digital Systems, WS/SS
- EI50141 Testing Digital Circuits, WS
- EI7439 Timing of Digital Circuits, WS
- EI5042 Project Laboratory IC Design, WS/SS
- EI7403 VHDL System Design Lab Ulf Schlichtmann, WS/SS
- EI77502 Seminar on Topics in Electronic Design Automation, WS
- EI77501 Seminar on Topics in Integrated System, WS

### **EI7355 Nanosystems, Becherer, WS/SS**

- EI70730 Memory Technology for Data Storage, WS/SS
- EI71079 CMOS Analog-to-Digital Converters, SS

## **EI7384 System on Chip-Technologies, Herkersdorf, WS**

- EI7271 Chip Multicore Processors, SS
- EI70630 HW/SW Codesign, WS/SS
- EI71035 Multi-Criteria Optimization and Decision Analysis for Embedded Systems Design, WS
- EI5077 System-on-Chip Platform, SS
- EI70640 Synthesis of Digital Systems, WS/SS
- EI71013 System Design for the Internet of Things, SS
- EI70730 Memory Technology for Data Storage, SS
- EI5042 Project Lab IC Design, WS/SS
- EI7402 SystemC Lab, WS/SS
- EI78031 Practical Training Project Integrated Systems, WS/SS
- EI7403 VHDL System Design Lab, WS/SS
- EI5069 Smart Card Lab, WS/SS